# 6-Pin DIP Zero-Cross Triac Driver Optocoupler (600 Volt Peak)



## MOC3061M, MOC3062M, MOC3063M, MOC3162M, MOC3163M

#### Description

The MOC306XM and MOC316XM devices consist of a GaAs infrared emitting diode optically coupled to a monolithic silicon detector performing the function of a zero voltage crossing bilateral triac driver.

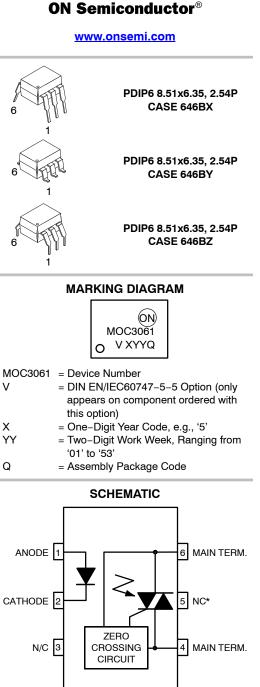
They are designed for use with a triac in the interface of logic systems to equipment powered from 115/240 VAC lines, such as solid-state relays, industrial controls, motors, solenoids and consumer appliances, etc.

#### Features

- Simplifies Logic Control of 115/240 VAC Power
- Zero Voltage Crossing to Minimize Conducted and Radiated Line Noise
- 600 V Peak Blocking Voltage
- Superior Static dv/dt
  - ♦ 600 V/µs (MOC306xM)
  - ◆ 1000 V/µs (MOC316xM)
- Safety and Regulatory Approvals
  - UL1577, 4,170 VAC<sub>RMS</sub> for 1 Minute
  - ◆ DIN EN/IEC60747-5-5
- These are Pb-Free Devices

#### Applications

- Solenoid/Valve Controls
- Static Power Switches
- Temperature Controls
- AC Motor Starters
- Lighting Controls
- AC Motor Drives
- E.M. Contactors
- Solid State Relays



\*DO NOT CONNECT (TRIAC SUBSTRATE)

#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 8 of this data sheet.

**SAFETY AND INSULATION RATINGS** (As per DIN EN/IEC 60747–5–5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.)

Parameter	Characteristics	
Installation Classifications per DIN VDE 0110/1.89 Table 1, For Rated Mains Voltage	<150 V <sub>RMS</sub>	I–IV
	<300 V <sub>RMS</sub>	I–IV
Climatic Classification		40/85/21
Pollution Degree (DIN VDE 0110/1.89)	2	
Comparative Tracking Index	175	

Symbol	Parameter	Value	Unit
V <sub>PR</sub>	Input-to-Output Test Voltage, Method A, $V_{IORM} \times 1.6 = V_{PR}$ , Type and Sample Test with $t_m = 10$ s, Partial Discharge < 5 pC	1360	V <sub>peak</sub>
	Input–to–Output Test Voltage, Method B, $V_{IORM} \times 1.875 = V_{PR}$ , 100% Production Test with $t_m = 1 \text{ s}$ , Partial Discharge < 5 pC	1594	V <sub>peak</sub>
VIORM	Maximum Working Insulation Voltage	850	V <sub>peak</sub>
V <sub>IOTM</sub>	Highest Allowable Over-Voltage	6000	V <sub>peak</sub>
	External Creepage	≥7	mm
	External Clearance	≥7	mm
	External Clearance (for Option TV, 0.4" Lead Spacing)	≥10	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥0.5	mm
R <sub>IO</sub>	Insulation Resistance at $T_S$ , $V_{IO}$ = 500 V	>10 <sup>9</sup>	Ω

#### **ABSOLUTE MAXIMUM RATINGS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Device	Value	Unit
TOTAL DEVIC	E			
T <sub>STG</sub>	Storage Temperature	All	-40 to +150	°C
T <sub>OPR</sub>	Operating Temperature	All	-40 to +85	°C
ТJ	Junction Temperature Range	All	-40 to +100	°C
T <sub>SOL</sub>	Lead Solder Temperature	All	260 for 10 seconds	°C
PD	Total Device Power Dissipation at 25°C Ambient	All	250	mW
	Derate Above 25°C		2.94	mW/°C
EMITTER				
١ <sub>F</sub>	Continuous Forward Current	All	60	mA
V <sub>R</sub>	Reverse Voltage	All	6	V
PD	Total Power Dissipation at 25°C Ambient	All	120	mW
	Derate Above 25°C		1.41	mW/°C
DETECTOR				
V <sub>DRM</sub>	Off-State Output Terminal Voltage	All	600	V
I <sub>TSM</sub>	Peak Non-Repetitive Surge Current (Single Cycle 60 Hz Sine Wave)	All	1	A <sub>peak</sub>
I <sub>TM</sub>	Peak Repetitive On-State Current	All	100	mA <sub>peak</sub>
PD	Total Power Dissipation at 25°C Ambient	All	150	mW
	Derate Above 25°C	7	1.76	mW/°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C, unless otherwise noted) Symbol Parameter **Test Conditions** Device Min Тур Max Unit INDIVIDUAL COMPONENT CHARACTERISTICS EMITTER VF Input Forward Voltage All V $I_{\rm F} = 30 \, {\rm mA}$ \_ 1.3 1.5 **Reverse Leakage Current** $V_{\rm B} = 6 V$ All 0.005 100 μA IR \_ DETECTOR Peak Blocking Current, V<sub>DRM</sub> = 600 V, I<sub>F</sub> = 0 (Note 1) MOC306XM 10 500 nA \_ I<sub>DRM1</sub> Either Direction MOC316XM 10 100 \_ Critical Rate of Rise of MOC306XM 600 dv/dt $I_{F} = 0$ (Note 2) 1500 V/µs Off-State Voltage MOC316XM 1000 \_ TRANSFER CHARACTERISTICS IFT LED Trigger Current Main Terminal Voltage = 3 V MOC3061M \_ 15 mΑ \_ (Rated I<sub>FT</sub>) (Note 3) MOC3062M \_ \_ 10 MOC3162M MOC3063M 5 \_ \_ MOC3163M V Vтм Peak On-State Voltage, ITM = 100 mA peak, IF = rated IFT All \_ 1.8 3.0 **Either Direction** Holding Current, Either All 500 $I_{H}$ μΑ \_ \_ Direction ZERO CROSSING CHARACTERISTICS Г V.... Inhibit Voltage (MT1-MT2 MOC3061M 12 20 ٧/

VINH	Voltage Above Which De- vice will not Trigger)	IF = Iaren IFT	MOC3062M MOC3063M	-	12	20	v
			MOC3162M MOC3163M	-	12	15	V
I <sub>DRM2</sub>	Leakage in Inhibited State	$I_F$ = rated $I_{FT}$ , $V_{DRM}$ = 600 V, off-state	All	-	_	2	mA

#### **ISOLATION CHARACTERISTICS**

V <sub>ISO</sub>	Isolation Voltage (Note 4)	f = 60 Hz, t = 1 Minute	4170	-	-	VAC <sub>RMS</sub>
R <sub>ISO</sub>	Isolation Resistance	$V_{I-O} = 500 V_{DC}$	-	1011	-	Ω
C <sub>ISO</sub>	Isolation Capacitance	V = 0 V, f = 1 MHz	-	0.2	-	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Test voltage must be applied within dv/dt rating.

2. This is static dv/dt. Commutating dv/dt is a function of the load-driving thyristor(s) only.

All devices are guaranteed to trigger at an I<sub>F</sub> value less than or equal to max I<sub>FT</sub>. Therefore, recommended operating I<sub>F</sub> lies between max I<sub>FT</sub> (15 mA for MOC3061M, 10 mA for MOC3062M and MOC3162M, 5 mA for MOC3063M and MOC3163M) and absolute maximum I<sub>F</sub> (60 mA).
 Isolation voltage, V<sub>ISO</sub>, is an internal device dielectric breakdown rating. For this test, pins 1 and 2 are common, and pins 4, 5 and 6 are common.

#### **TYPICAL PERFORMANCE CURVES**

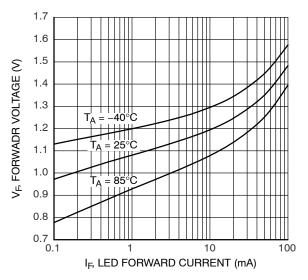


Figure 1. LED Forward Voltage vs. Forward Current

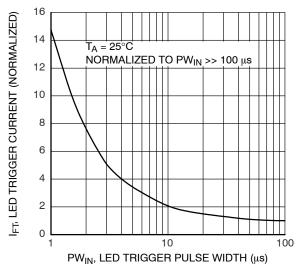


Figure 3. LED Current Required to Trigger vs. LED Pulse Width

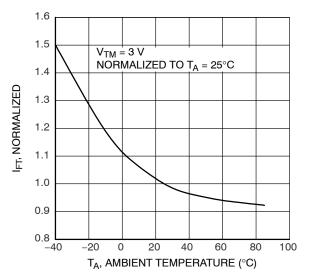


Figure 2. Trigger Current Vs. Temperature

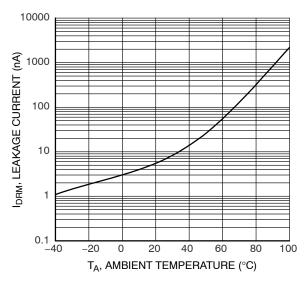
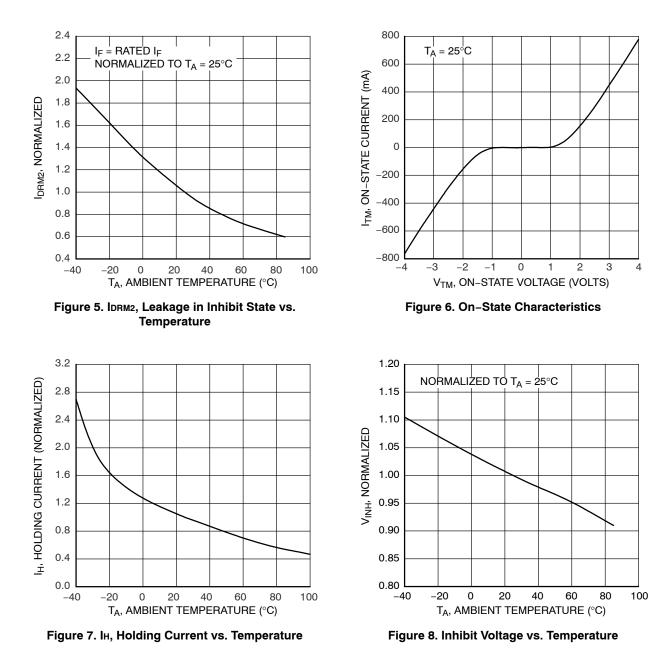


Figure 4. Leakage Current, I<sub>DRM</sub> vs. Temperature

#### TYPICAL PERFORMANCE CURVES (Continued)



#### **APPLICATION INFORMATION**

#### **Basic Applications**

Typical circuit for use when hot line switching is required. In this circuit the "hot" side of the line is switched and the load connected to the cold or neutral side. The load may be connected to either the neutral or hot line.

Rin is calculated so that  $I_F$  is equal to the rated  $I_{FT}$  of the part, 15 mA for the MOC3061M, 10 mA for the MOC3062M, or 5 mA for the MOC3063M.

The 39  $\Omega$  resistor and 0.01  $\mu$ F capacitor are for snubbing of the triac and is often, but not always, necessary depending upon the particular triac and load used.

Suggested method of firing two, back-to-back SCR's with a ON Semiconductor triac driver. Diodes can be 1N4001; resistors, R1 and R2, are optional  $330 \Omega$ .

NOTE: This optoisolator should not be used to drive a load directly. It is intended to be a trigger device only.

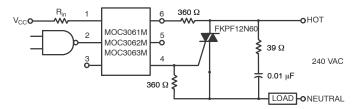


Figure 9. Hot-Line Switching Application Circuit

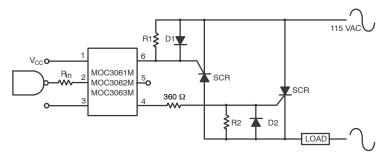
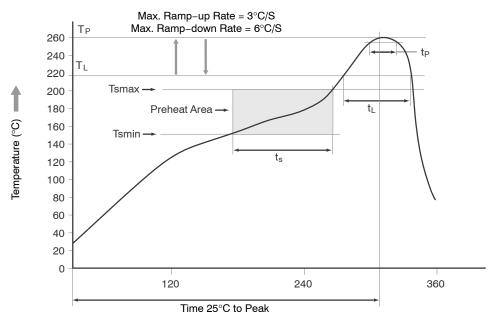


Figure 10. Inverse-Parallel SCR Driver Circuit



Time (seconds)

Profile Freature	Pb-Free Assembly Profile
Temperature Minimum (Tsmin)	150°C
Temperature Maximum (Tsmax)	200°C
Time (t <sub>S</sub> ) from (Tsmin to Tsmax)	60 seconds to 120 seconds
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/second maximum
Liquidous Temperature (TL)	217°C
Time (t <sub>L</sub> ) Maintained Above (T <sub>L</sub> )	60 seconds to 150 seconds
Peak Body Package Temperature	260°C +0°C / -5°C
Time (t <sub>P</sub> ) within 5°C of 260°C	30 seconds
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/second maximum
Time 25°C to Peak Temperature	8 minutes maximum

Figure 11. Reflow Profile

#### **ORDERING INFORMATION** (Note 5)

Part Number	Package	Shipping <sup>†</sup>
MOC3061M	DIP 6-Pin (Pb-Free)	50 Units / Tube
MOC3061SM	SMT 6–Pin (Lead Bend) (Pb–Free)	50 Units / Tube
MOC3061SR2M	SMT 6-Pin (Lead Bend) (Pb-Free)	1000 / Tape & Reel
MOC3061VM	A DIP 6-Pin, DIN EN/IEC60747-5-5 Option (Pb-Free)	
MOC3061SVM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option 50 Units / T (Pb-Free) 50 Units / T	
MOC3061SR2VM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option (Pb-Free)	1000 / Tape & Reel
MOC3061TVM	DIP 6-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 Option (Pb-Free)	50 Units / Tube

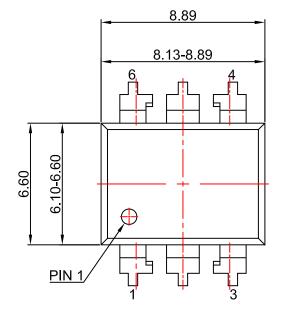
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
5. The product orderable part number system listed in this table also applies to the MOC3062M, MOC3063M, MOC3162M, and MOC3163M

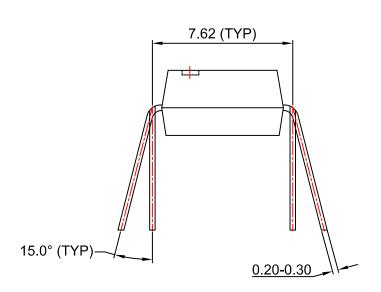
product families.

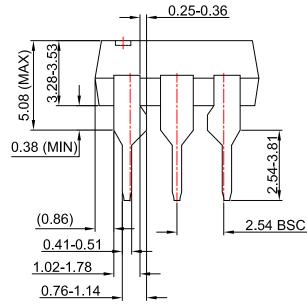


PDIP6 8.51x6.35, 2.54P CASE 646BX ISSUE O

DATE 31 JUL 2016







#### NOTES:

- A) NO STANDARD APPLIES TO THIS PACKAGE.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSION

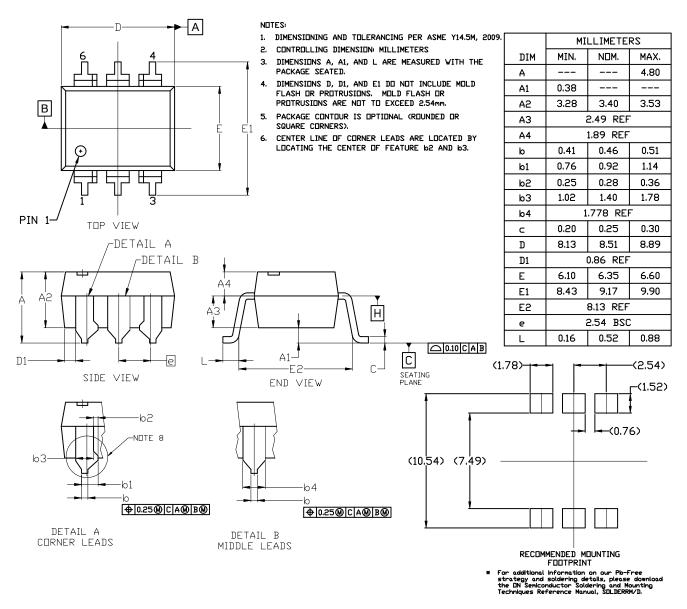
DOCUMENT NUMBER:	98AON13449G	Electronic versions are uncontrolled except when accessed directly from the Document Repository Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	SCRIPTION: PDIP6 8.51X6.35, 2.54P		PAGE 1 OF 1	
ON Semiconductor and water and the semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability including without limitation special consequential or includental damages. ON Semiconductor does not convey any license under its nater it context in the test of the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor does not convey any license under its nater it context on the semiconductor its nater its				

rights of others.

# ON

#### PDIP6 8.51x6.35, 2.54P CASE 646BY ISSUE A

#### DATE 15 JUL 2019

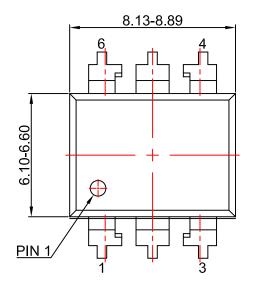


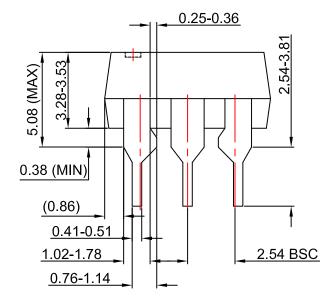
DOCUMENT NUMBER:	98AON13450G	Electronic versions are uncontrolled except when accessed directly from the Document Repository Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.			
DESCRIPTION: PDIP6 8.51x6.35, 2.54P PAGE 1 O		PAGE 1 OF 1			
ON Semiconductor and (iii) are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights nor the					

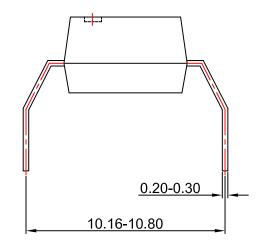


PDIP6 8.51x6.35, 2.54P CASE 646BZ ISSUE O

DATE 31 JUL 2016







NOTES:

- A) NO STANDARD APPLIES TO THIS PACKAGE.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSION

DOCUMENT NUMBER:	98AON13451G	Electronic versions are uncontrolled except when accessed directly from the Document Repository Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.			
DESCRIPTION:	DESCRIPTION: PDIP6 8.51X6.35, 2.54P PA		PAGE 1 OF 1		
ON Semiconductor and I are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights or others.					

onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and calcular performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

#### TECHNICAL SUPPORT

onsemi Website: www.onsemi.com

Email Requests to: orderlit@onsemi.com

North American Technical Support: Voice Mail: 1 800-282-9855 Toll Free USA/Canada Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support: Phone: 00421 33 790 2910 For additional information, please contact your local Sales Representative

# **Mouser Electronics**

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

onsemi:

 MOC3061TVM
 MOC3061SVM
 MOC3062M
 MOC3061M
 MOC3062TVM
 MOC3163SR2M
 MOC3063SR2M

 MOC3062SVM
 MOC3061SR2M
 MOC3162TVM
 MOC3162M
 MOC3061VM
 MOC3061SM

 MOC3062SR2VM
 MOC3162SR2VM
 MOC3162VM
 MOC3163SVM
 MOC3061VM
 MOC3061SM

 MOC3062SR2VM
 MOC3162SR2VM
 MOC3162VM
 MOC3163SVM
 MOC3062SM
 MOC3063M
 MOC3163TVM

 MOC3162SVM
 MOC3163SR2VM
 MOC3063SR2VM
 MOC3061SR2VM
 MOC3063SM
 MOC3063SM
 MOC3063TVM

 MOC3162SR2M
 MOC3063SR2VM
 MOC3061SR2VM
 MOC3061SR2VM
 MOC3063SVM
 MOC3063SVM

 MOC3162SR2M
 MOC3063SR2VM
 MOC3061SR2VM
 MOC3061SR2VM
 MOC3063SVM
 MOC3063SVM