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DMG80480T043_09WTR

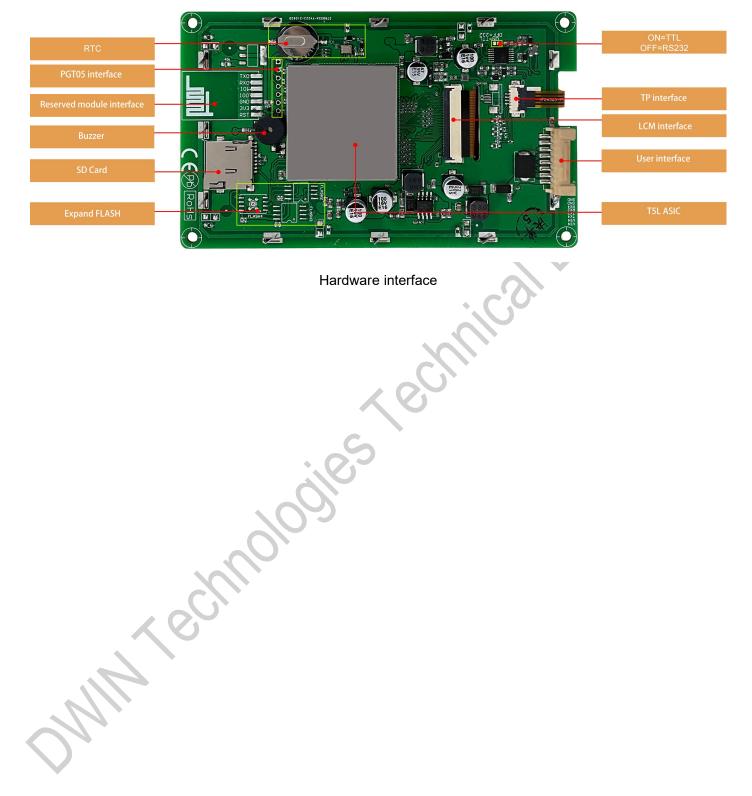
Features:

- Based on T5L0, running DGUS II system, industrial grade.
- 4.3-inch, 480*800 pixels resolution, 262K colors, IPS-TFT-LCD, wide viewing angle.
- Resistive touch screen.
- Highlight 720nit.
- Anti-UV, with conformal coating.



1. Hardware and interface

1.1 Hardware interface



1.2 Interface description

No.	Name	Description		
1	T5L0 ASIC	Developed by DWIN. Mass production in 2020,1MBytes Nor Flash on the chip, 512KBytes used to store the user database. Rewrite cycle: over 100,000 times		
2	LCM interface	FPC40_0.5mm, RGB interface		
3	RTP interface	4Pin_1.0mm interface		
4	User interface	8Pin 2.0mm socket for power supply and serial communication.		
5	Flash	16MBytes NOR Flash, for fonts, pictures and audio files. Rewrite cycle: over 100,000 times		
6	Expand Flash	Expandable to 64Mbytes NOR Flash or 48Mbytes NOR Flash+512Mbytes NAND Flash		
7	Buzzer	3V passive buzzer. Power: <1W		
8	RTC	Super-capacitor for power supply. Accuracy: 1s/24h. It can work normally for 30 days after power failure		
9	SD interfaceFAT32. Download files by SD interface can be displayed in statistics. Download rate: 4Mb/s			
10 Reserved module		Wi-Fi module: connect to the cloud platform to update remotely		
	interface	USB module: download files by USB flash disk		
11	11 PGT05 interface When product crashes by accident, you can use PGT05 to update I kernel and make the product return to normal			

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2. Specification parameters

2.1 Display parameters

LCD Type	IPS, TFT LCD		
Viewing Angle	Wide viewing angle, 85°/85°/85°(L/R/U/D)		
Resolution	480×800 pixels (support 0°/90°/180°/270°)		
Color	18-bit 6R6G6B		
Active Area (A.A.)	93.60mm (W)×56.16mm (H)		
View Area (V.A.)	-		
Backlight Mode	LED		
Backlight Service Life>30000 hours (Time of the brightness decaying to 50% on the concentration of the maximum brightness)			
Brightness	720nit		
Brightness Control	0~100 grade (When the brightness is adjusted to 1%~30% of the maximum brightness, flickering may occur and is not recommended to use in this range)		
	of high contrast still image over 30 minutes may lead to display residual en saver to avoid this problem.		

2.2 Touch parameters

Type RTP (Resistive touch panel)		
Structure	ITO film + ITO glass	
Touch Mode	Support point touch and drag	
Surface Hardness	ЗН	
Light Transmittance	Over 80%	
Life	Over 1,000,000 times touch	

2.3 Serial interface parameters

Mode	UART2: ON=TTL/CMOS; OFF=RS232 UART4: ON=TTL/CMOS; OFF=RS232 (Only available after OS configuration)					
	Test Condition	Min	Тур	Max	Unit	
	Output 1, lout = 1mA	3.0	3.3	-	V	
Voltage Level	Output 0, lout = -1mA	-	0	0.3	V	
	Input 1, lin = 1mA	2.4	3.3	5.0	V	
	Input 0, lin = -1mA	0	-	0.5	V	
Baud Rate	3150~3225600bps, typical value of 115200bps					
UART2: N81						
Data Format	UART4: N81/E81/O81/N82, 4 modes (OS configuration)					
Interface Cable	8Pin_2.0mm					
2.4 Electrical specifications						

2.4 Electrical specifications

Rated Power	<5W		
Operating Voltage	5∼36V, typical value of 12V		
Operating Current	- VCC=12V, max backlight - VCC=12V, backlight off		
Recommended power supply: 12V 1A DC			

2.5 Operating environment

Operating Temperature -20℃~70℃ (12V @ 60% RH)	
Storage Temperature	-30℃~80℃
Anti-UV Yes	
Conformal coating	Yes
Operating Humidity	10%~90%RH, typical value of 60% RH

3. Reliability test

3.1 Electrostatic discharge test

Test temperature: 25°C. Test humidity: 50%RH.

Test process: the product was placed on the test bench to perform contact and air discharge in turn of the serial screen iron frame and display area as shown in Fig.3.1 below. During the experimental process, it was observed whether the screen is dead, black, white, splash, or reboot. According to the experiment results, the performance is in line with the criteria GB/T 17626.2 B level and above.



3.1 Electrostatic discharge test

Discharge Type	Discharge Value	Result
Contact discharge	±6KV	Normal operation
Air discharge	±8KV	Normal operation

3.2 EFT test

Test temperature: 25°C. Test humidity: 50%RH.

Test process: the product was placed on the test bench to perform contact and the smart screen is energized by the power supply coupled with a EFT generator as shown in Fig. 3.2 below. During the experimental process, it was observed whether abnormal reset, display or touch phenomena occurs. According to the experiment results, the performance is in line with the criteria GB/T 17626.2 B level and above.



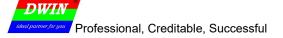
3.2 EFT test

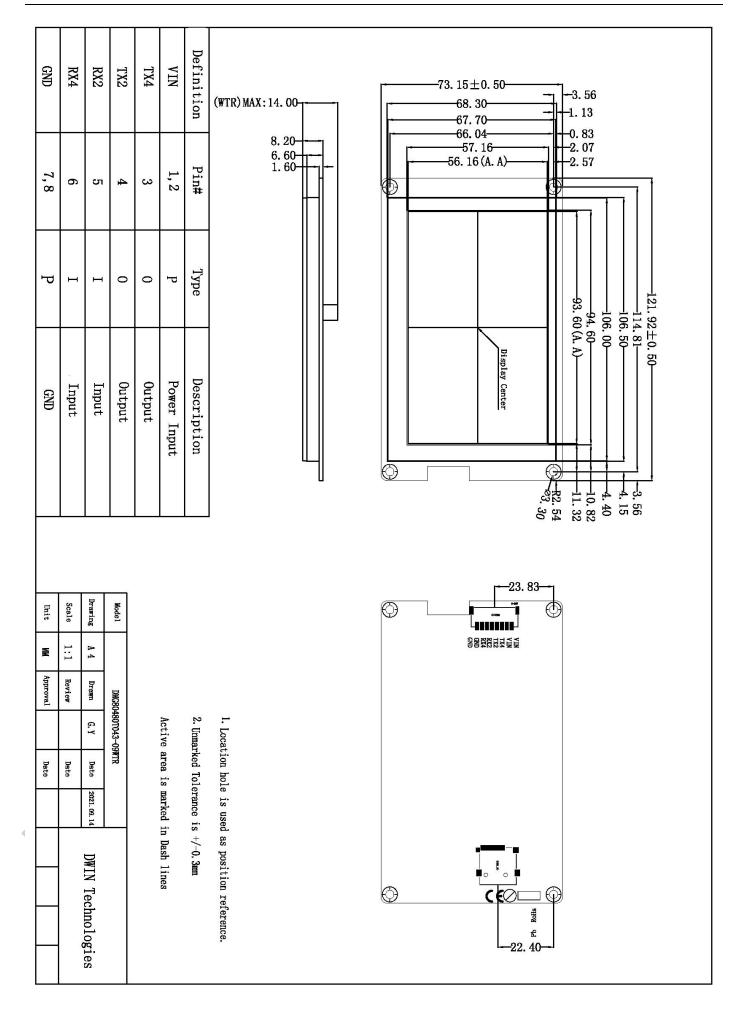
Test Item	Test Standard	Result
Power supply	±2KV;100KHz	Normal operation

4. Packaging & dimensions

Form Factor				
	121.9mm (W)×73.2mm (H)×14.0mm(T)			
Installation Dimensions	Positioning hole: 106.5(+0.3mm)×68.3(+0.3mm)			
Net Weight	- <80			
Packaging Standar	ds			
Model	Dimensions	Layer	Quantity/Layer	Quantity(Pcs)
Carton1:	220mm(L)×160mm(W)×47mm (H)	1	2	2
Carton2:	250mm(L)×200mm(W)×80mm (H)	2	2	4
Carton3:	320mm(L)×270mm(W)×80mm (H)	2	4	8
Carton4:	435mm(L)×335mm(W)×290mm(H)	2	25	50
Carton5:	600mm(L)×430mm(W)×290mm(H)	2	60	120

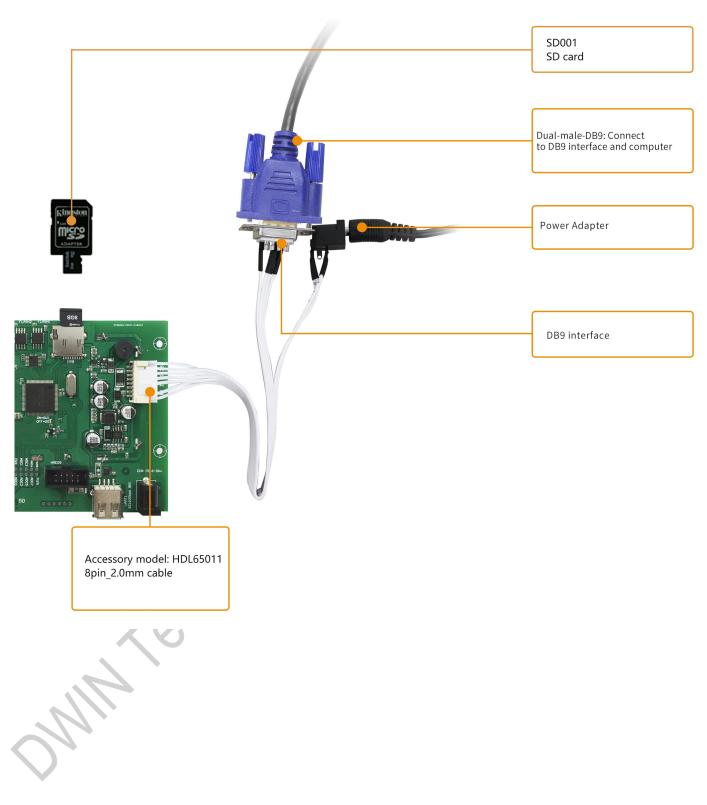
Disclaimer: the data is for reference only and the information of product design that do not affect performance parameters and utilization is subject to alternation without prior notice.





5. Debugging tools

It is recommended for new users of DWIN smart LCMs to purchase a test kit. For more details, please refer to customer service center.



6. T5L series IC features

(1) Mature and stable 8051 core which is the most widely used with the maximum operating frequency of T5L is up to 250MHz, 1T(single instruction cycle)high speed operation.

(2) Separate GUI CPU Core running DGUS II System:

- High-speed display memory, 2.4GB/S bandwidth.
- 2D hardware acceleration, the decompression speed of JPEG is up to 200fps@1280*800 and the UI with animation and icons as its main feature is extremely cool and smooth.
- Images and icons stored in JPEG format. Adopt Low-cost 16Mbytes SPI Flash.
- Support CTP or RTP with adjustable sensitivity and maximum 400 Hz touch frequency.
- 1-way 15bit 32Ksps PWM digital power amplifier driver loudspeaker, save power amplifier cost and achieve high signal-to-noise ratio and sound quality restoration.
- 128Kbytes variable storage space for exchanging data with OS CPU Core and memory.
- Support DGUS development and simulation on PC. Support background remote upgrade.

(3) Separate CPU (OS CPU) core runs user 8051 code or DWIN OS system and user CPU is omitted in practical application:

- Standard 8051 architecture and instruction set, 64Kbytes code space, 32Kbytes on-chip RAM.
- 64 bit integer mathematical operation unit (MDU), including 64 bit MAC and 64 bit divider.
- 28 IOs, 4-channel UARTs, 1-channel CAN, up to 8-channel 12-bit A/Ds and 2-channel 16-bit PWM of adjustable resolution.
- Support IAP on-line simulation and debugging with unlimited number of breakpoints.
- Upgrade code online through DGUS system.
- 1Mbytes on-chip Flash with DWIN patent encryption technology ensure code and data security.
- Operating temperature ranges from -40 °C to +85 °C (IC operating temperature customizable from -55 °C to 105 °C).

DWIN encourages users to design your own customized product based on T5L.



7. Revision records

Rev	Revise Date	Content	Editor
00	2021-09-25	First Edition	Zheng Yunjia

Please contact us if you have any questions about the use of this document or our products, or if you would like to

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Thank you all for continuous support of DWIN, and your approval is the driving force of our progress!

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